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Participation Fee Rs. 2500/-

Registration is open for EEE & ECE Engineering faculty (limited to 30 members).

Registration fee is to be paid through Demand Draft drawn in favour of

Principal, NMAMIT, Nitte payable at Syndicate Bank, Nitte.

To Register, log on to <http://www.nmamit.nitte.edu.in/upevents.php> and fill the

Google Form

Accommodation

Accommodation shall be arranged on prior request and to be indicated in the Registration Form. Limited accommodation is available which can be booked on first come first serve basis with nominal charges.

Last Date for Registration 15 July, 2018

Contact

Dr. Suryanarayana K

Associate Professor, Department of EEE, NMAMIT, Nitte

Mobile: +91-9945569606

e-mail: suryanarayana@nitte.edu.in

 **NITTE** | **N.M.A.M. INSTITUTE OF TECHNOLOGY**
EDUCATION TRUST (An Autonomous Institution affiliated to VTU, Belagavi)
Nitte - 574110, Karnataka, India



Faculty Development Programme on

DESIGN, ANALYSES & HARDWARE IMPLEMENTATION OF SWITCHED MODE POWER CONVERTERS

23 to 28 July, 2018

Organized by

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

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Mangalore Sub-Section
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SCHEDULE

Day 1		
Sl. No.	Time	Topics
1	9:00-10:00 AM	Registration
2	10:00-11:00 AM	Inauguration
3	11:15-12:45 PM	Keynote Address <i>Mr. Milind Dighrasker,</i> Co-Founder, Enstin Labs, Bengaluru
4	1:45-3:00 PM	Introduction to Orcad
5	3:15-4:30 PM	<ul style="list-style-type: none"> • Component Selection • Datasheet Data • Part Creation • Schematic Entry • Footprint Creation
Day 2		
6	9:00-10:45 AM	Introduction to Power Converters
7	11:00-12:30 PM	DC-DC Converter Design - Buck
8	1:15-2:15 PM	Role of Engineers in the society <i>Dr. Niranjan N. Chiplunkar</i> Principal, NMAMIT, Nitte
9	2:15-4:30 PM	Layout <ul style="list-style-type: none"> • Schematic import to Layout • Steps in Layout • Routing and Gerber Creation
Day 3		
10	9:00-10:45 AM	DC-DC Converter Modeling
12	11:00-12:30 PM	Stability Aspects of DC-DC Converter and VMC
13	1:15-2:45 PM	Inductor Design and Winding using EE Core
14	3:00-4:30 PM	Buck Converter - Implementation Perspective <ul style="list-style-type: none"> • Schematic Entry • BoM Generation • Netlist Verification • Layout
Day 4		
15	9:00-11:00 AM	PCB Assembly and Testing - Open Loop
16	11:15-1:00 PM	Layout of Closed Loop Buck Converter
17	2:00-4:30 PM	Closed Loop Circuit Assembly and Testing
Day 5		
18	9:00-1:00 PM	TI PMLK BUCK <i>Mr. Shanmuga Sundaram</i> Director, Steps Knowledge Services (P) Ltd
19	2:00-4:30 PM	Measurement challenges in validating Power Converters <i>Mr. Jatan Shah</i> Account Manager, Tektronix India Pvt. Ltd.
Day 6		
20	9:00-11:00 AM	Demonstration of Prototype
21	11:15-12:00 PM	Valedictory

About NMAMIT

Situated in the Sylvan surroundings at the foot of the Western Ghats, the Nitte village is 57kms from Mangaluru city. Nitte Mahalinga Adyanthaya Memorial Institute of Technology (NMAMIT), an Autonomous Institution affiliated to Visvesvaraya Technological University (VTU) Belagavi, is a premier Institute of Nitte Education Trust (NET). The Institution, is recognised by the All India Council for Technical Education, New Delhi, is named after the first ever Engineering graduate from coastal Karnataka. The NET, (established in 1979) runs 34 Educational Institutions, five of which are at Nitte village providing serenity of the rural atmosphere with all the facilities of an urban centre. The College offers seven UG and eleven PG courses in the Engineering disciplines besides MCA and MBA courses. All the engineering branches have been recognized as Research Centres by VTU and many scholars are pursuing their research work. Institute is accredited by NAAC with 'A' grade. The Institution has been granted Academic Autonomy under the Visvesvaraya Technological University from the academic year 2007.

About the Department

Department of Electrical & Electronics Engineering, established in the year 1987, offers UG and PG courses. The Department has been offering M.Tech in Power Electronics (Autonomous under VTU) from the academic year 2014-15. The Department is accredited by NBA during the year 2018. Equipped with the state of the art Laboratory in Power Electronics, Control Systems, Power Systems and High Voltage & Relay, the Department caters to the need of all Electrical and Electronics Engineering students. "Center for Design of Power Electronics Systems" in the Research and Innovation center provides opportunity to carryout advanced studies in the field of power electronics.

FDP OUTLINE

Present day trend demands the use of Power Electronics from sub-watt to Megawatt range. Study & analyses of Power Electronics systems has become an important area and makes way for the extensive research. Design and control of Power Electronics systems is a challenge for the research scholars and academicians. This Faculty Development Programme aims at providing exposure to control aspects of power converters. Association of industries like - Texas Instruments, Tektronix, IMedrix and Convergent Technologies along with IEEE Mangalore Sub-Section strengthens the capabilities of the programme.

Hands-on sessions will provide deeper insight into schematic Capture, PCB layout, PCB assembly and Testing. The program will provide platform to design and develop application specific systems.

BOOK-POST

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