About the Institute

NMAM Institute of Technology, Nitte, an autonomous Institution under Visvesvarava Technological University (VTU), Belagavi was established in the year 1986 in memory of Nitte Mahalinga Adyanthaya, a distinguished engineer of the erstwhile Dakshina Kannada district in Karnataka. It is one of the constituent Institutions under Nitte Education Trust, Mangalore, which runs about 26 Institutions in various areas of education right from primary school to post-graduation centers, including professional courses like Engineering, Management, Medical, Nursing, Dental, Paramedical and others. Nitte University, a NAAC accredited A-grade University is run by the Nitte Education Trust founded by late Justice K.S. Hegde, a well-known judge of Supreme Court of India and former speaker of Indian Lok Sabha. The institute is recognized by the All India Council for Technical Education, New Delhi and majority of the programmes are NBA accredited as OBE under Tier-1. The institution is ISO 9001:2015 certified from NVT Quality Certification (ANAB Accredited) and is recognized as a lead Institute under TEQIP, a world bank sponsored project executed through MHRD, New Delhi. The Institute is having MOUs for research and project collaborations with Pennsylvania State University, USA, Ritsumeikan University, Japan and National University of Singapore, Singapore.

NMAM Institute of Technology, Nitte offers undergraduate programmes and postgraduate programmes in seven branches of engineering viz: Bio-Technology, Civil Engineering, Computer Science & Engineering, Electronics & Communication Engineering, Electrical & Electronics Engineering, Information Science & Engineering, and Mechanical Engineering. All departments also have been recognized as research centers for offering M.Sc. (Engg.) & Ph.D. programmes under VTU. The Institute also offers post-graduation and research programmes in Computer Applications and Business Management and Ph.D. programmes in Basic Sciences and Mathematics.

The Institute is located on the Padubidri-Karkala State Highway which is well connected with Mangalore (60KM) and Udupi/Manipal (45Km) through NH66 & NH169. The participants can alight at Mangalore or Udupi Railway Stations/Bus Terminals, or Mangalore Airport to reach the Institute.

About the Department

Established in 1986, the Department of Electronics & Communication offers an Undergraduate Program with an intake of 180 students and Two Postgraduate Programs in Digital Electronics & Communication Engineering with an intake of 25 students and VLSI Design & Embedded Systems with an intake of 18 Students. The department is well-equipped with modern laboratories and has well-qualified and experienced faculty members.

About Entuple Technologies, Bangalore

Entuple is a next generation solutions enabler in cutting edge technologies. Head Quartered at Bangalore, India, Entuple delivers world class products & solutions in Applied Electromagnetics, Semiconductor and System Design & Reliability, Drivers & Power Systems, Telecom, Mechanical and Civil sector. It offers R & D consulting across technologies. Its niche services include - IC fabrication alliance, Post silicon validation and more. It caters to wide range of customers in semiconductor, manufacturing, defense & aerospace and academia.

Outcome of the workshop

At the end of the workshop the participant will be able to:

• Analyze the performance specification

requirements and identify the suitable circuit topologies

- Characterize the PDK for device analog model parameters
- Synthesize basic and OP-AMP amplifier circuit topologies
- Design CMOS Amplifier circuits for given DC and AC performance parameters
- Derive layout constraints for the physical design of the OP-AMP and carry out DRC and LVS
- Carry out the physical verification and parasitic extraction
- Back Annotate and carry out Post Layout Simulation/Characterization
- Get familiar with Electronic system Design flow
- Analyze the Board System Design flow using OrCAD P-spice and OrCAD-PCB
- Understand the need of Prototyping solutions in Electronic System Design and Manufacturing

Registration:

Registration Fee:

Faculty, Research Scholars Rs. 1500/-M. Tech. Students: Rs 1000/-

Last date for Registration: 28th July 2017

Accommodation:

Accommodation will be arranged on prior request for nominal charges.

Eligibility:

Faculty, Research Scholars and PG students associated with VLSI Domain.

NMAM Institute of Technology, Nitte

Department of

Electronics & Communication Engineering FIVE DAY WORKSHOP ON

Analog IC Design & System Design Using



Cadence & OrCAD P-Spice July 31st to 4th August 2017



REGISTRATION FORM

Name :					
Designation :					
Department :					
Name of the Institut	ion:				
Mobile :					
E-mail :					
Place:					
Date :					
Signature of the	Signature of				
Applicant	Head of the Institution with Seal				
	nned copy of the duly filled to pradyumna@nitte.edu.in				
Payment shall be rem	nitted through Electronic Fund				
Transfer Facility to					
Account Holder :	Principal, NMAM Institute of Technology, Nitte				
Account Type :	C 3 ·				
Account No. :					
Name of the Bank:	Syndicate Bank, Nitte Branch, Nitte - 574 110,				
	Karnataka, INDIA				
IFSC/RTGS Code :					
	SYNBINBB014				
MICR Code :	576025533				
Note: Please add "A					
(de	legate's name)" in the Remarks/				
Narration col	umn.				

Chief Patron

Sri N. Vinaya Hegde

President, NET

Patron

Dr. Niranjan N. Chiplunkar Principal

Convener

Dr. Rekha Bhandarkar

Professor & Head

Coordinators

Mr. Mahaveera K Mr. Pradyumna G R

Contact Details

Mr. Mahavera K.

Assistant Professor
Dept. of ECE, NMAMIT, Nitte
mahaveera_nmamit @nitte.edu.in
9880370386

Mr. Pradyumna G. R.

Assistant Professor Dept. of ECE, NMAMIT, Nitte pradyumna@nitte.edu.in 9686423889





N.M.A.M. Institute of Technology Nitte - 574110

(An Autonomous Institution Under VTU, Belagavi) (A Unit of Nitte Education Trust)

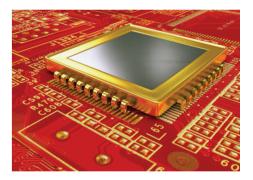
Karkala Tq., Udupi District, Karnataka.

Phone: 08258-281264 Fax: 08258-281265

FIVE DAY WORKSHOP ON

Analog IC Design & System Design Using Cadence & OrCAD P-Spice

July 31st to 4th August 2017



Organized By

Department of Electronics and Communication Engineering N.M.A.M. Institute of Technology, Nitte

In association with

Entuple Technologies, Bangalore





FIVE DAY WORKSHOP ON

Analog IC Design & System Design Using Cadence & OrCAD P-Spice

July 31st to 4th August 2017

Го							
	 		 	•••••	•••••	•••••	
	 	••••••	 				



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NMAM Institute of Technology, Nitte - 574110

(An Autonomous Institute under VTU, Belagavi)

(A unit of Nitte Education Trust)

Karkala Tq., Udupi District, Karnataka State, India